

APPLICATION DATA SHEET

APPLICATION INFORMATION

Application Type::	REGULAR
Subject Matter::	UTILITY
CD-ROM or CD-R?::	NONE
Title::	SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND STANDARD CELL PLACEMENT DESIGN METHOD
Attorney Docket Number::	250061US2S DIV
Total Drawing Sheets::	4
Small Entity?::	NO

INVENTOR INFORMATION

Applicant Authority Type::	INVENTOR
Primary Citizenship Country::	Japan
Status::	FULL CAPACITY
Given Name::	Shinsuke
Family Name::	SAKAMOTO
City of Residence::	Yokohama-shi
Country of Residence::	Japan
Street of Mailing Address::	c/o Intellectual Property Division, KABUSHIKI KAISHA TOSHIBA 1-1 Shibaura 1-chome, Minato-ku
City of Mailing Address::	Tokyo
Country of Mailing Address::	Japan
Postal or Zip Code of Mailing Address::	105-8001

Applicant Authority Type:: INVENTOR
 Primary Citizenship Country:: Japan
 Status:: FULL CAPACITY
 Given Name:: Akira
 Family Name:: YAMAGUCHI
 City of Residence:: Kitaadachi-gun
 Country of Residence:: Japan
 Street of Mailing Address:: c/o Intellectual Property Division,
 KABUSHIKI KAISHA TOSHIBA
 1-1 Shibaura 1-chome, Minato-ku
 City of Mailing Address:: Tokyo
 Country of Mailing Address:: Japan
 Postal or Zip Code of Mailing Address:: 105-8001

CORRESPONDENCE INFORMATION

Correspondence Customer Number:: 22850

REPRESENTATIVE INFORMATION

Representative Customer Number:: 22850

DOMESTIC PRIORITY INFORMATION

Application::	Continuity Type::	Parent Application::	Parent Filing Date::
This Application	Division of	10/282,144	10/29/02

FOREIGN PRIORITY INFORMATION

Application Number:	Country::	Filing Date::	Priority Claimed::
2001-331294	Japan	10/29/01	YES